

Form PTO-1449

Atty Docket No.

Serial No.

P-9197

10/611,443

Applicant(s)

Swee Yew Choe

Filing Date

June 30, 2003

Group

~~2836~~ 2816

INFORMATION DISCLOSURE CITATION
IN AN APPLICATION

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>W</i>	AA	4,247,791	01/27/81	Rovell	307	238	
<i>W</i>	AB	5,859,548	01/12/99	Kong	326	113	
<i>W</i>	AD	6,028,454	02/22/00	Elmasry et al.	326	115	
<i>W</i>	AC	6,211,704 B1	04/03/01	Kong	326	121	
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

							Translation	
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO
	AL							
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

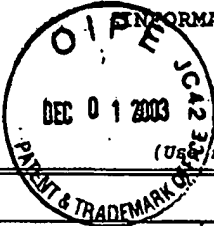
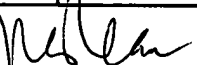
<i>W</i>	AR	Choe et al., "Dynamic Half Rail Differential Logic for Low Power", IEEE 1997, pages 1936 to 1939
<i>W</i>	AS	Jung et al., "Modular Charge Recycling Pass Transistor Logic (MCRPL)", Electronics Letters, 2nd March 2000 Vol. 36 No.5, March 2, 2000, Pages 404 to 405
<i>W</i>	AT	Kong et al., "Charge Recycling Differential Logic for Low-Power Application", ISSC96 secession 18, IEEE 0-780331962/98, 1998, Pages 302 to 448

Examiner *W*

Date Considered

2/22/2005

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)								
76	AR	Choe et al., "Half Rail Differential Logic", ISSCC97/Secession 25/Processors and Logic/Paper SP 25.6 IEEE 0-7803-3721-2/97, 1997, pages 420 to 421, 336 to 337 and 489						
u	AS	Won et al., "Modified Half Rail Differential Logic for Reduced Internal Logic Swing", IEEE 0-7803-4455-3/98, 1998, pages II-157 to II-160						
u	AT	Kong et al., "Charge Recycling Differential Logic (CRDL) for Low-Power Application", IEEE Journal of Solid-State Circuits, Vol. 31, No. 9, September 1996, pages 1267 to 1276						
Examiner 		Date Considered 2/22/2005						
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